ONFI Technical Errata

Errata ID	**017**
Affected Spec Ver.	**ONFI 3.0, 3.1, 3.2, 4.0**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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This errata changes the tRST(max) value from 10us to 15us for the case when the target is not performing an erase or program operation.

Description of the specification change

Section 4.17.1 shown below (informative):

There are three maximums listed for tRST in the SDR, NV-DDR, NV-DDR2 and NV-DDR3 data interfaces. The target is allowed a longer maximum reset time when a program or erase operation is in progress. The maximums correspond to:

1. The target is not performing an erase or program operation.
2. The target is performing a program operation.
3. The target is performing an erase operation.

Modify Section 4.18.1 (Table 83) shown below:

Parameter	Mode 0		Mode 1		Mode 2		Unit
	100		50		35		ns
	Min	Max	Min	Max	Min	Max	
tRST (raw NAND)	—	5000	—	~~10~~15/30/ 500	—	~~10~~15/30/ 500	µs

Modify Section 4.18.1 (Table 84) shown below:

Parameter	Mode 3		Mode 4 (EDO capable)		Mode 5 (EDO capable)		Unit
	100		50		35		ns
	Min	Max	Min	Max	Min	Max	
tRST (raw NAND)	—	5000	—	~~10~~15/30/ 500	—	~~10~~15/30/ 500	µs

Modify Section 4.18.2 (Table 85) shown below:

Parameter	Mode 0		Mode 1		Mode 2		Mode 3		Mode 4		Mode 5		Unit
	50		30		20		15		12		10		ns
	20		~33		50		~66		~83		100		MHz
	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
tRST (raw NAND)	—	~~10~~15/30/ 500	—	~~10~~15/30/ 500	—	~~10~~15/30/ 500	—	~~10~~15/30/ 500	—	~~10~~15/30/ 500	—	~~10~~15/30/ 500	µs

Modify Section 4.18.3 (Table 86) shown below:

Constant Timing Parameter Values			
	Min	Max	Unit
tRST	—	~~10~~15/30/500	µs

Disposition log

4/5/2016	Erratum captured.

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