ONFI Technical Errata

Errata ID	**003**
Affected Spec Ver.	**ONFI 2.0, 2.1, 2.2, 3.0, 3.1, 3.2, 4.0**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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The erratum adds an AC noise limitation for power supply voltages. Previously there was no limitation for AC noise within the specified operating power supply range. This erratum limits the AC noise for 3.3V operation to +/- 100mV and the AC noise for 1.8V and 1.2V operation to +/- 50mV. AC noise is defined as 250 kHz to 20 MHz.

Description of the specification change

Modify Section 2.10 Table 7 shown below:

2.10 Recommended DC Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units
Supply voltage for 3.3V devices[4]	V_{CC}	2.7	3.3	3.6	V
Supply voltage for 1.8V devices[5]	V_{CC}	1.7	1.8	1.95	V
Supply voltage for 3.3V I/O signaling[1,4]	V_{CCQ}	2.7	3.3	3.6	V
Supply voltage for 1.8V I/O signaling[5]	V_{CCQ}	1.7	1.8	1.95	V
Supply voltage for 1.2V I/O signaling[5]	V_{CCQ}	1.14	1.2	1.26	V
Ground voltage supply	V_{SS}	0	0	0	V
Ground voltage supply for I/O signaling	V_{SSQ}	0	0	0	V
External voltage supply[3]	V_{PP}	10.8	12.0	13.2	V

NOTE:
1. 3.3V VccQ is not supported for NV-DDR2.
2. 3.3V and 1.8V VccQ are not supported for NV-DDR3.
3. The maximum external voltage supply current (I_{PP}) is 5 mA per LUN.
4. AC Noise on the supply voltages shall not exceed +/- 100mV (250 kHz to 20 MHz). AC and DC noise together shall stay within the Min-Max range specified in this table.
5. AC Noise on the supply voltages shall not exceed +/- 50mV (250 kHz to 20 MHz). AC and DC noise together shall stay within the Min-Max range specified in this table.

Table 7 Recommended DC operating conditions

Disposition log

8/8/2014	Erratum captured.

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