ONFI Technical Errata

Errata ID	**024**
Affected Spec Ver.	**ONFI 3.0, 3.1, 3.2, 4.0**
Corrected Spec Ver.	

Submission info

Name	Company	Date
Kenneth Koenig	Micron	07/03/17
Anthony Constantine	Intel	07/25/17

This errata updates tJITcc spec values from min to max values.

Description of the specification change

Replace table 87 "NV-DDR2/NV-DDR3 Timing Parameter Values" with updated table below. tJITcc(DQS) and tJITcc(RE_n) values moves to the Max column -vs- the Min column.

Timing Mode Specific Values (Modes 0-3)									
	Mode 0		**Mode 1**		**Mode 2**		**Mode 3**		**Unit**
	30		25		15		12		ns
	~ 33		40		~ 66		~ 83		MHz
	Min	Max	Min	Max	Min	Max	Min	Max	
tJITper (DQS)	-2.4	2.4	-2.0	2.0	-1.2	1.2	-1.0	1.0	ns
tJITper (RE_n)	-1.8	1.8	-1.5	1.5	-0.9	0.9	-0.75	0.75	ns
tJITcc (DQS)	—	4.8	—	4.0	—	2.4	—	2.0	ns
tJITcc (RE_n)	—	3.6	—	3.0	—	1.8	—	1.5	ns
Timing Mode Specific Values (Modes 4-7)									
	Mode 4		**Mode 5**		**Mode 6**		**Mode 7**		**Unit**
	10		7.5		6		5		ns
	100		~133		~166		200		MHz
	Min	Max	Min	Max	Min	Max	Min	Max	
tJITper(DQS)	-0.80	0.80	-0.60	0.60	-0.48	0.48	-0.40	0.40	ns
tJITper(RE_n)	-0.60	0.60	-0.45	0.45	-0.36	0.36	-0.30	0.30	ns
tJITcc(DQS)	—	1.6	—	1.2	—	0.96	—	0.8	ns
tJITcc(RE_n)	—	1.2	—	0.9	—	0.72	—	0.6	ns
Timing Mode Specific Values (Modes 8-10)									
	Mode 8		**Mode 9**		**Mode 10**				**Unit**
	3.75		3		2.5				ns
	~267		~333		400				MHz
	Min	Max	Min	Max	Min	Max			
tJITper(DQS)	-0.30	0.30	-0.24	0.24	-0.2	0.2			ns
tJITper(RE_n)	-0.225	0.225	-0.18	0.18	-0.15	0.15			ns
tJITcc(DQS)	—	0.6	—	0.48	—	0.4			ns
tJITcc(RE_n)	—	0.45	—	0.36	—	0.3			ns

Disposition log

07/03/2017	Erratum captured.
07/25/2017	Per feedback on reflector plus technical meeting, changed from min/max to max only.

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