ONFI Technical Errata

Errata ID	**023**
Affected Spec Ver.	**ONFI 3.0, 3.1, 3.2, 4.0**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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This errata updates Slew Rate Definition for Differential Input Signals for NV-DDR2/3.

Description of the specification change

Add Figure XX "Differential Input Slew Rate Definition" shown below after Table 62 NV-DDR3 Input Slew Rate Derating Values for DQ and single-ended DQS:

Description	Measured		Defined by
	From	To	
Differential input slew rate for rising edge (DQS_t – DQS_c)	VILdiff(DC)	VIHdiff(DC)	[VIHdiff(DC) – VILdiff(DC)] / Delta tRISEdiff
Differential input slew rate for falling edge (DQS_t – DQS_c)	VIHdiff(DC)	VILdiff(DC)	[VIHdiff(DC) – VILdiff(DC)] / Delta tFALLdiff
NOTE: The differential signal must be linear between these thresholds.			

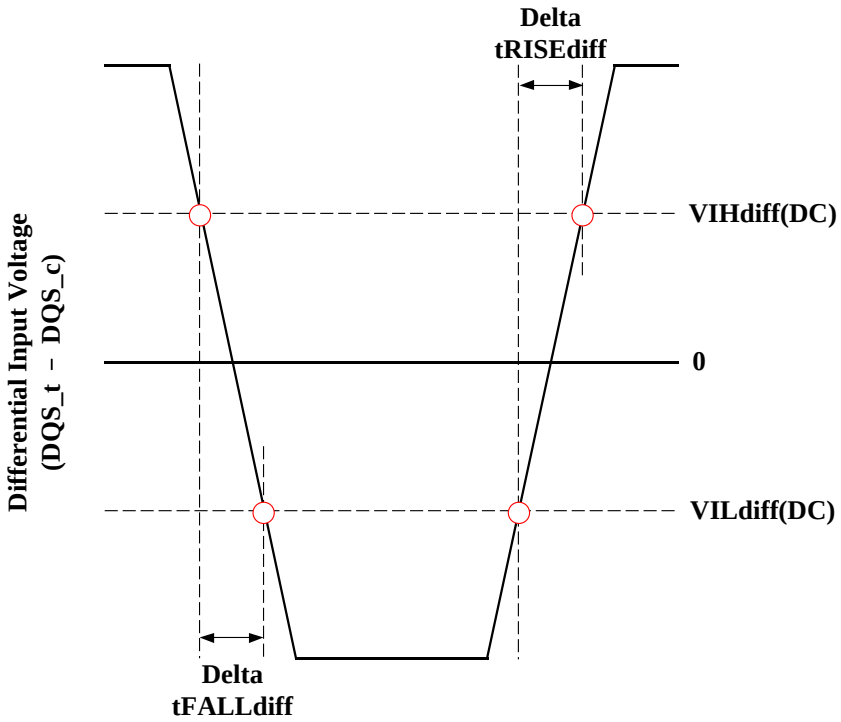

Figure Differential Input Slew Rate Definition for DQS_t, DQS_c

Disposition log

06/20/2017	Erratum captured.

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