ONFI Technical Errata

Errata ID	**002**
Affected Spec Ver.	**ONFI 1.0 2.0, 2.1, 2.2, 2.3, 3.0, 3.1**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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This erratum reduces the minimum input capacitance values. This is an important change to Improve signal integrity and lower package cost.

Description of the specification change

Modify Table 36 as shown:

Parameter	Source Synchronous Timing Modes 0-5				Unit
	Min	Typ Low	Typ High	Max	
1 LUN per x8 data bus	1.5 ~~2.0~~	2.5 ~~3.0~~	5.4	6.4	pF
2 LUNs per x8 data bus	2.0 ~~3.5~~	3.5 ~~5.0~~	7.7	9.2	pF
4 LUNs per x8 data bus	3.5 ~~5.0~~	5.5 ~~7.0~~	12.3	14.3	pF
8 LUNs per x8 data bus	4.5 ~~6.0~~	8.5 ~~10.0~~	23.3	27.3	pF
EZ NAND	3.0	5.0	6.5	8.5	pF

Table 36 Input Capacitance Typical Ranges for Input Pins

Disposition log

08/13/2012	Erratum captured.

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