ONFI Technical Errata

Errata ID	**007**
Affected Spec Ver.	**ONFI 2.0, 2.1, 2.2**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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Currently there is no minimum time defined for CE# remaining high. This erratum defines the CE# high time parameter (tCEH) with a value of 20 ns for all timing modes.

The erratum also clarifies the CE# falling edge placement when using CE# don't care for asynchronous data input and output.

Description of the specification technical flaw

Add tCEH definition to Table 19 and Table 24:

Parameter	Description
tCEH	CE# high hold time

Table 19 Asynchronous Timing Parameter Descriptions

Parameter	Description
tCEH	CE# high hold time

Table 24 Source Synchronous Timing Parameter Descriptions

Add tCEH definition to Table 22, Table 23 and Table 39:

Parameter	Mode 0		Mode 1		Mode 2		Unit
	100		50		35		ns
	Min	Max	Min	Max	Min	Max	
tCEH	20	—	20	—	20	—	ns

Table 22 Asynchronous Timing Modes 0, 1, and 2

Parameter	Mode 3		Mode 4 (EDO capable)		Mode 5 (EDO capable)		Unit
	30		25		20		ns
	Min	Max	Min	Max	Min	Max	
tCEH	20	—	20	—	20	—	ns

Table 23 Asynchronous Timing Modes 3, 4, and 5

Parameter	Mode 0		Mode 1		Mode 2		Mode 3		Mode 4		Mode 5		Unit
	50		30		20		15		12		10		ns
	~20		~33		~50		~66		~83		~100		MHz
	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
tCEH	20	—	20	—	20	—	20	—	20	—	20	—	ns

Table 39 Source Synchronous Timing Modes

Clarify CE# don't care for data input and data output cycles:

4.3.1.3 Data Input Cycle Timings

Data input may be used with CE# don't care. However, if CE# don't care is used tCS and tCH timing requirements shall be met by the host, and WE# falling edge shall always occur after the CE# falling edge (i.e. tCS > tWP).

4.3.1.4 Data Output Cycle Timings

Data output may be used with CE# don't care. However, if CE# don't care is used tCEA and tCOH timing requirements shall be met by the host and RE# shall either remain low or RE# falling edge shall occur after the CE# falling edge.

Disposition log

03/02/2010	Erratum captured.
05/03/2010	Erratum ratified.

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