ONFI Technical Errata

Errata ID	**005**
Affected Spec Ver.	**ONFI 2.1**
Corrected Spec Ver.	

Submission info

Name	Company	Date
Amber Huffman	Intel	04/07/2009

Description of the specification technical flaw (add space as needed)

There are four instances in the specification where “Celsius” is misspelled as “Celcius”. This erratum corrects those misspellings.

In the parameter page, the interleaved operations field has an attribute that is not appropriately named. The “Address restrictions for program cache” attribute is an attribute that applies to both program cache and read cache. This erratum generalizes that attribute.

Description of the correction

Update Table 23 in section 4.2.3 as shown:

Condition	Temperature (TA)	VccQ (3.3V)	VccQ (1.8V)	Process
Minimum Impedance	TOPER (Min) degrees ~~Celcius~~ Celsius	3.6V	1.95V	Fast-fast
Nominal Impedance	25 degrees ~~Celcius~~ Celsius	3.3V	1.8V	Typical
Maximum impedance	TOPER (Max) degrees ~~Celcius~~ Celsius	2.7V	1.7V	Slow-slow

Table 23 Testing Conditions for Impedance Values

Update the paragraph following Table 32 in section 4.2.3 as shown:

The input capacitance requirements are defined in Table 33. The testing conditions that shall be used to verify the input capacitance requirements are: temperature of 25 degrees ~~Celcius~~ Celsius, V_{IN} = 0V, and a CLK frequency of 100 MHz. The capacitance delta values measure the pin-to-pin capacitance for the same LUN within a Target.

Update byte 114 in section 5.6.1 as shown:

114	O	Interleaved operation attributes 5-7 Reserved (0) 4 1 = read cache supported 3 Address restrictions for ~~program~~ cache operations 2 1 = program cache supported 1 1 = no block address restrictions 0 Overlapped / concurrent interleaving support

Disposition log

4/7/2009	Erratum captured.
5/28/2009	Erratum ratified.

Technical input submitted to the ONFI Workgroup is subject to the terms of the ONFI contributor's agreement.