ONFI Technical Errata

Errata ID	**008**
Affected Spec Ver.	**ONFI 2.0 and ONFI 2.1**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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This erratum corrects the variance for the CLK input capacitance typical value reported in the parameter page. This correction is consistent with the variance listed in section 4.2.3 of the specification.

Description of the specification technical flaw

Update section 5.6.1.38 as shown:

5.6.1.38 Byte 144-145: CLK input pin capacitance, typical

This field indicates the typical CLK input pin capacitance for the target. This value applies to the CLK and CLK# signals. This field is specified in 0.1 pF units. For example, a value of 31 corresponds to 3.1 pF. The variance from this value is less than +/- ~~0.25~~ 0.5 pF per LUN per x8 data bus. As an example, if two LUNs are present per x8 data bus than the total variance is less than +/- ~~0.5~~ 1.0 pF. This value is only valid if the typical capacitance values are supported as indicated in the source synchronous features field. Additional constraints on the CLK input pin capacitance are specified in section 4.2.3.

Disposition log

5/21/2009	Erratum captured.
7/21/2009	Erratum ratified.

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