ONFI Technical Errata

Errata ID	**010**
Affected Spec Ver.	**ONFI 2.0 and ONFI 2.1**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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There was an error in the specifications for output slew rate. The output slew rate specifications incorrectly specify input reference voltages. This erratum corrects the output slew rate testing conditions.

Description of the specification technical flaw

Update Table 30 of section 4.2.3 as shown:

Parameter	Value
VOL(DC)	0.3 * VccQ
VOH(DC)	0.7 * VccQ
VOL(AC)	0.2 * VccQ
VOH(AC)	0.8 * VccQ
Positive input transition	VOL ~~VIL~~ (DC) to VOH ~~VIH~~ (AC)
Negative input transition	VOH ~~VIH~~ (DC) to VOL ~~VIL~~ (AC)
tRISE	Time during rising edge from VOL(DC) to VOH(AC)
tFALL	Time during falling edge from VOH(DC) to VOL(AC)
Output slew rate rising edge	VOH(AC) – VOL(DC) / tRISE
Output slew rate falling edge	VOH(DC) – VOL(AC) / tFALL
Output capacitive load	CL = 5 pF

NOTE:
1. Output slew rate is verified by design and characterization; it may not be subject to production test.
2. The minimum slew rate is the minimum of the rising edge and the falling edge slew rate. The maximum slew rate is the maximum of the rising edge and the falling edge slew rate.

Table 30 Testing Conditions for Output Slew Rate

Add the following diagram after Table 30 in section 4.2.3 as shown:

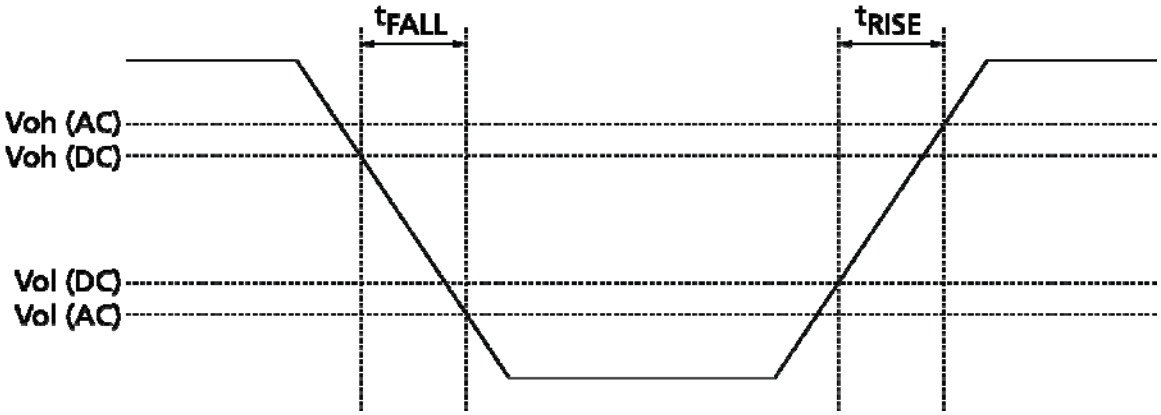

Figure 21 tRISE and tFALL Definition for Output Slew Rate

Disposition log

7/7/2009	Erratum captured.
9/12/2009	Erratum ratified.

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