ONFI Technical Errata

Errata ID	**007**
Affected Spec Ver.	**ONFI 2.0**
Corrected Spec Ver.	

Submission info

Name	Company	Date
Amber Huffman	Intel	7/28/2008

Description of the specification technical flaw (add space as needed)

This erratum clarifies that the CE# signal is high for the staggered power-up current requirement.

Description of the correction

Modify the IST test conditions in Table 7 in section 2.10 as shown:

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Staggered power-up current	IST	CE#=VccQ-0.2V, tRise = 1 ms, cLine = 0.1 µF			10	mA

Table 7 DC and Operating Conditions, measured on Vcc rail

Disposition log

7/28/2008	Erratum captured.
10/15/2008	Erratum ratified.

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