ONFI Technical Errata

Errata ID	**008**
Affected Spec Ver.	**ONFI 1.0 and ONFI 2.0**
Corrected Spec Ver.	

Submission info

Name	Company	Date
Amber Huffman	Intel	9/17/2008

Description of the specification technical flaw (add space as needed)

This erratum corrects the behavioral state machine. The idle states in the LUN behavioral flow update the RDY bit in the status register of the LUN to one continuously. If the LUN is directed by the target to clear the RDY bit to zero and then the LUN reverts to the idle state, the change requested by the target is lost.

The erratum removes the continual status register update. Any status register updates are done explicitly as part of the action rather than automatically in the idle states.

Description of the correction

Changes are shown using ONFI 2.0 section numbers. The erratum applies to the ONFI 1.0 specification also.

Modify the L_Idle state in section 7.2.2 as shown:

L_Idle[1]	~~lunStatus[6] is set to one. lunStatus[6] value is indicated to the Target.~~ lunReturnState is set to L_Idle.		
	1. Target request received	→	L_Idle_TargetRequest
	NOTE: 1. This state is entered as a result of a power-on event when Vcc reaches Vcc_min.		

Modify the L_Idle state in section 7.2.3 as shown:

L_Idle_Rd	~~lunStatus[6] is set to one. lunStatus[6] value is indicated to the Target.~~ lunReturnState is set to L_Idle_Rd.		
	1. Background read operation complete	→	L_Idle_Rd_Finish
	2. Target requests column address be selected	→	L_Idle_Rd_ColSelect
	3. Read request received from Target	→	L_Idle_Rd_Xfer
	4. Command cycle 31h (Read Cache Sequential) received	→	L_RD_Cache_Next
	5. Command cycle 3Fh (Read Cache End) received and lunLastConfirm is 31h	→	L_RD_Cache_Xfer_End
	6. Target request received	→	L_Idle_TargetRequest

Disposition log

9/17/2008	Erratum captured.
11/3/2008	Erratum ratified.

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