ONFI Technical Errata

Errata ID	**001**
Affected Spec Ver.	**ONFI 1.0**
Corrected Spec Ver.	

Submission info

Name	Company	Date
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Description of the specification technical flaw (add space as needed)

There are a few clarifications required in the behavioral flow.

The first update is to the LUN idle read state. In this state, the Target request arc precedes a few caching arcs. The Target request should be the last arc to avoid interfering with the read cache arcs.

In the LUN read states, there is not a state that records the address received from the Target. This erratum adds a state that records this information for later use.

Description of the correction

Modify state L_Idle_Rd in section 7.2.3 as shown:

L_Idle_Rd	lunStatus[6] is set to one. lunStatus[6] value is indicated to the Target. lunReturnState is set to L_Idle_Rd.		
	1. Background read operation complete	→	L_Idle_Rd_Finish
	2. Target requests column address be selected	→	L_Idle_Rd_ColSelect
	3. Read request received from Target	→	L_Idle_Rd_Xfer
	~~4. Target request received~~	~~→~~	~~L_Idle_TargetRequest~~
	4. Command cycle 31h (Read Cache) received	→	L_RD_Cache_Next
	5. Command cycle 3Fh (Read Cache End) received and lunLastConfirm is 31h	→	L_RD_Cache_Xfer_End
	6. Target request received	→	L_Idle_TargetRequest

Add state L_RD_Addr in section 7.2.7 as shown:

L_RD_Addr	The LUN performs the following actions in the order specified: 1. Records address received from the Target. 2. If interleaved addressing is supported, selects the correct page register based on the interleaved address. 3. Selects the column in the page register based on the column address received.		
	1. Unconditional	→	L_RD_WaitForCmd

Modify state L_Idle_TargetRequest in section 7.2.2 as shown:

L_Idle_TargetRequest	If Target indicates an address, the address is stored by the LUN.		
	1. Target requests LUN perform a Reset	→	L_RST_Execute
	2. Target indicates WP# value	→	L_WP_Update
	3. Target requests SR register update	→	L_SR_Update
	4. Target requests status or status command received	→	L_Status_Execute
	5. Target indicates output buffer should be turned off	→	L_Idle
	6. Target requests page register clear	→	L_Idle_ClearPageReg
	7. Target requests page register invalidate	→	L_Idle_InvalidPageReg
	8. Target indicates Program request for this LUN	→	L_PP_Execute
	9. Target indicates Erase request for this LUN	→	L_BE_Execute
	10. Target indicates Read Page request for this LUN	→	~~L_RD_WaitForCmd~~ L_RD_Addr
	11. Target indicates Read Parameter Page request	→	L_Idle_RdPp
	12. Target indicates Read Unique ID request	→	L_Idle_RdUid

Disposition log

12/17/2006	Erratum captured
3/4/2007	Erratum ratified

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